IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Ap	oplication of:	• ·
Kenichi	i KADOTA	
Application No.: Not Yet Assigned) Group Art Unit: Not yet assigned)
Filed: I	February 24, 2004)	Examiner: Not yet assigned
,	ANALYSIS METHOD FOR SEMICONDUCTOR DEVICE, ANALYSIS SYSTEM AND A COMPUTER PROGRAM PRODUCT	

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicant brings to the Examiner's attention the documents listed on attached Form PTO-1449. Copies of the listed documents are attached. Applicant respectfully requests that the Examiner consider the documents listed on attached Form PTO-1449 and indicate that they were considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

FINNEGAN HENDERSON FARABOW GARRETT & DUNNER LP

1300 I Street, NW Washington, DC 20005 202.408.4000 Fax 202.408.4400 www.finnegan.com

Customer No. 22,852 Attorney Docket No. 03180.0354

The following are listed on the accompanying PTO-1449 and are in a non-English language. English-language abstract are attached.

- 1. Japanese Patent Application Laid-Open Publication No. 2001-110867 This document discloses a quality control system analyzing a relationship between yield deterioration and a process parameter. Also, the relevance of this document is discussed at page 3 of the specification of the present application.
- Japanese Patent Application Laid-Open Publication No. 2003-37143 This document discloses systematic yield loss areas on a wafer are extracted automatically.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and applicant determine that the cited documents do not constitute "prior art" under United States law, applicant reserves the right to present to the Office the relevant facts and law regarding the appropriate status of such documents. Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

FINNEGAN HENDERSON FARABOW GARRETT & DUNNER LL

1300 I Street, NW Washington, DC 20005 202.408.4000 Fax 202.408.4400 www.finnegan.com

Customer No. 22,852 Attorney Docket No. 03180.0354

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,

GARRETT & DUNNER, L.L.P.

Dated: February 24, 2004

Richard V. Burgujian ,

Reg. No. 31,744

Enclosures RVB/FPD/cma

FINNEGAN HENDERSON FARABOW GARRETT & DUNNERLLP

1300 I Street, NW Washington, DC 20005 202.408.4000 Fax 202.408.4400 www.finnegan.com

INFORMATION DISCLOSURE CITATION

Atty. Docket No.	03180.0354	Application No.	ТВА	
Applicant	Kenichi KADOTA			
Filing Date	February 24, 2004	Group:	ТВА	

U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
			-			
						· · · · · · · · · · · · · · · · · · ·

FOREIGN PATENT DOCUMENTS						
	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
	2001-110867	04/20/2001	Japan			Abstract
	2003-037143	02/07/2003	Japan			Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)				

Examiner		Date Considered	
*Examiner:		erence considered, whether or not citation is in conformance with MPEP 609; draw line ation if not in conformance and not considered. Include copy of this form with next ation to applicant.	
Form PTO 144	9 Patent and	Trademark Office - U.S. Department of Commerce	